Experiences with Building Domain-Specific Compilation Plugins in Graal

ManLang'17, 28 Sep 2017

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Is there a way to create domain-specific compiler optimizations without having to learn the whole compilation stack?

Yes! Modular JIT compilers (e.g. Graal)

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Introduction

- Computer vision applications becoming mainstream (e.g. autonomous vehicles, virtual reality)
- Both on embedded and desktop environments
- Ongoing effort to:
  - Increase accuracy
  - Optimize performance

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Background

Simultaneous Localization And Mapping (SLAM) Applications

Input

Stream of frames from cameras moving in an unknown environment

Output

- 3D reconstruction of environment
- Cameras' location in the environment
- Absolute positions of objects in the environment

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Our case

Large-**S**cale **D**irect monocular SLAM (LSD-SLAM)

- Monocular: uses a single camera for input
- Non feature-based, operates on image densities
- Uses pose-graphs

Pose-graph

A graph where:

- nodes are frames
- directed edges contain the transformations (rotation, scaling, and translation) and the corresponding covariance matrix from the previous frame

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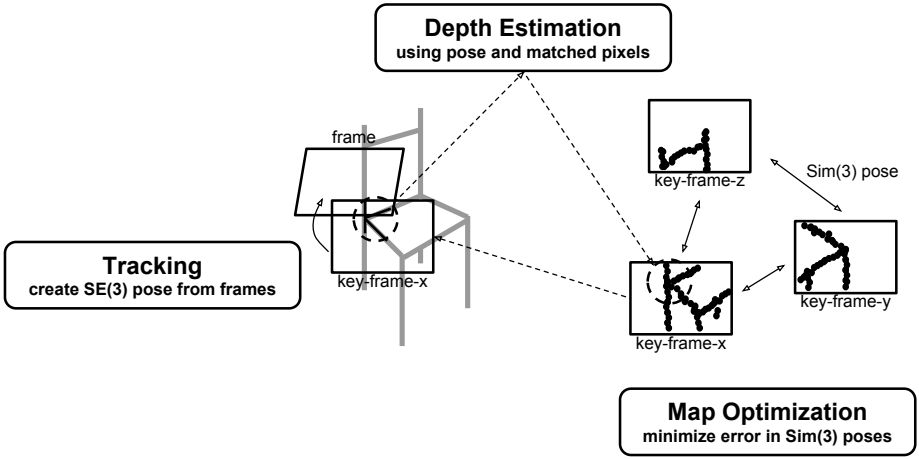
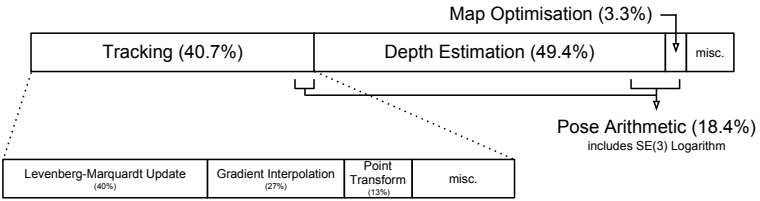
LSD-SLAM overview



LSD-SLAM breakdown



Framework	Point Trans. *mean (ns)*	SE(3) Log. *mean (ns)*	Gradient Inter. *mean (ns)*	L-M Update *mean (ns)*
Eigen (C++)	13.342	131.138	9.847	152.376
EJML (Java)	77.411	415.924	84.479	308.412
JEigen (JNI)	1356.498	1671.105	58.961	895.845

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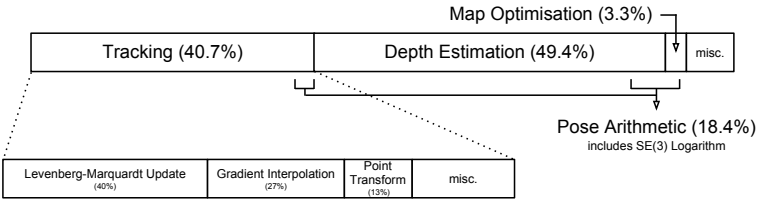

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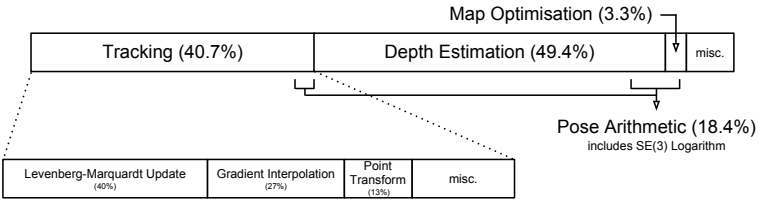

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Performance Characterization

JIT compiler generated code worse than the hand-tuned Eigen

- JIT compiler fails to inline some methods in the critical path
- Opportunities for constant folding and sub-expression elimination are missed
- No SIMD

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Indigo: Our Approach

A small vector and matrix library

- Up to 8 elements and 8x8 cells

Accompanied by a Graal plugin

- Force inline methods of the library
- Custom register allocation
- SIMD backend

Encapsulated and immutable

- Reduces object allocation
- Reduces memory indirection
- Enables constant folding
- Enhances common sub-expression elimination

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Why a new backend and register allocator?

1. There is no publicly accessible SIMD assembler in Graal
2. The JVM does not support SIMD registers
3. The JVM cannot handle SIMD registers during register spillage

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Indigo: Assumptions for SIMD acceleration

- Hardware supports 128-bit vector operations
- Indigo's classes/subclasses contain single-precision floating point numbers suitable for vector operations in SLAM
- Unused elements of a vector are zero
- The elements of a vector are contiguous in memory
- Once constructed, a vector is immutable

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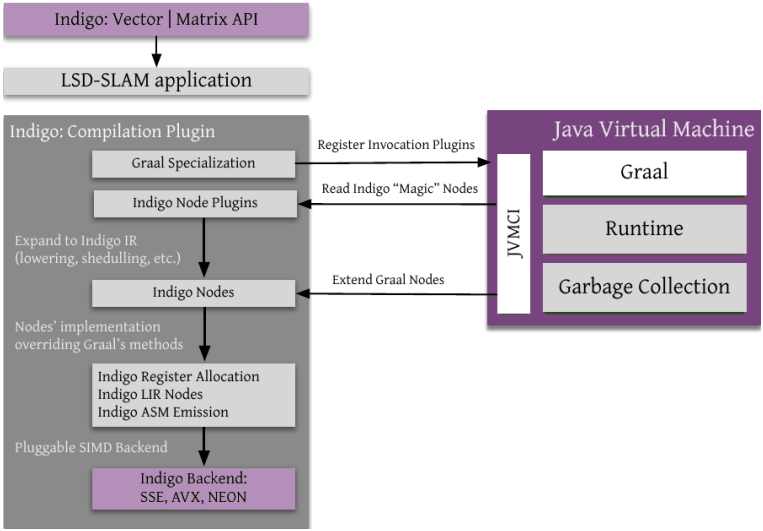
Indigo Compilation Plugin Outline



Methodology

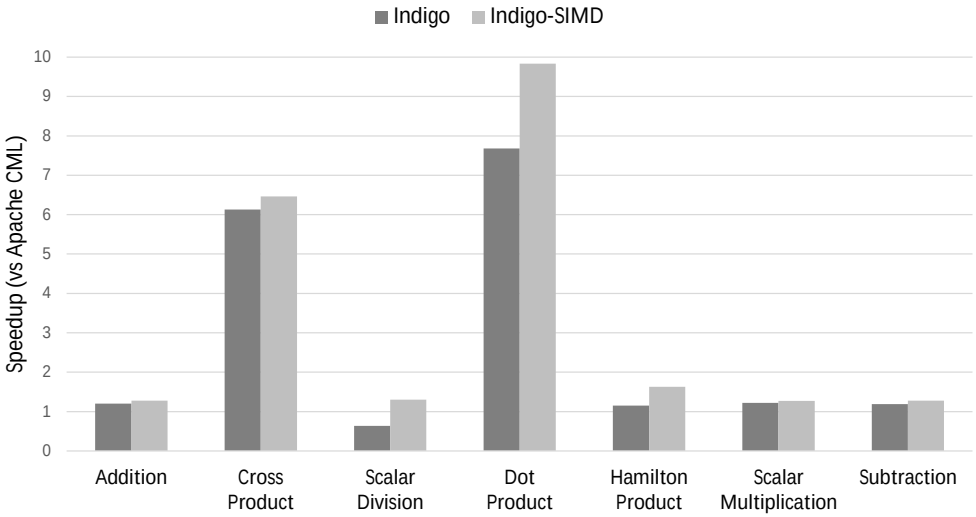
Comparison

1. Indigo vs Apache CML
   as a generic small vectors and matrices Java library
2. Indigo vs Eigen
   as a SLAM specific library

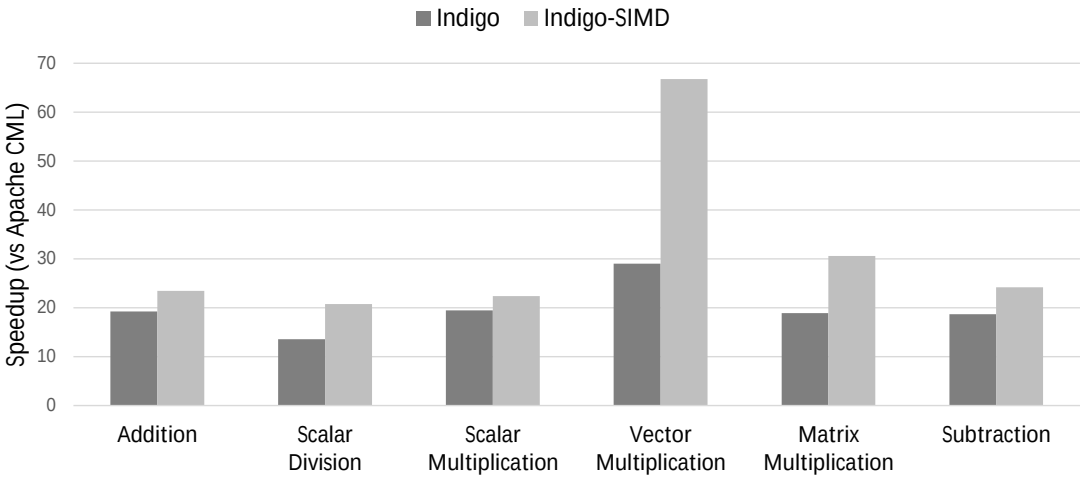
Evaluation Setup

Hardware	
Processor	Intel Core i7 4770 3.4GHz
Cores	4
Hardware threads	8
Main memory	16GB
Vector Units	SSE 4.2 and AVX2
Software	
OS	Windows 8.1
C++ compiler	MSVC 17.00.61030 (x64)
JVM	Java SE 1.8.0_72 64-Bit JVMCI VM
Baseline	Apache CML 3.6

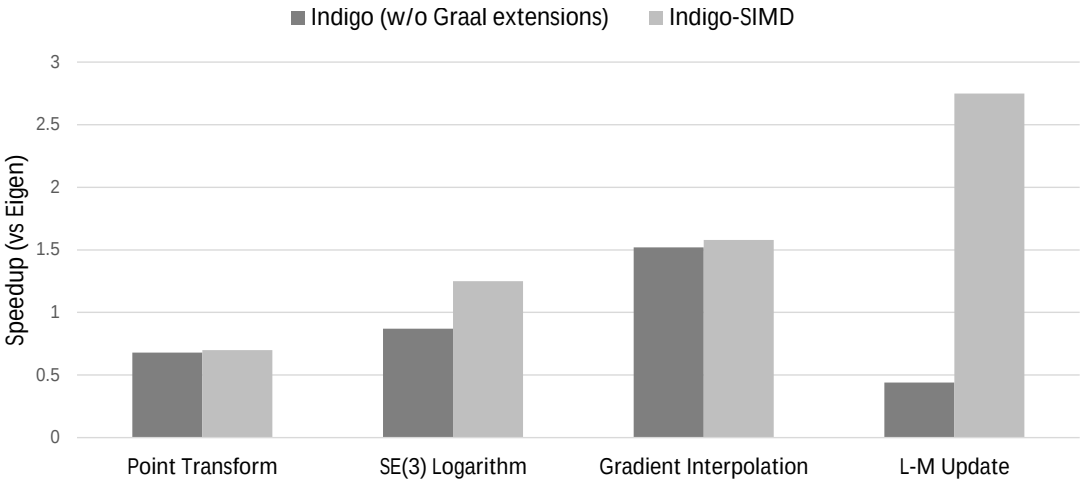
Indigo vs Apache CML: Vector Operations



Indigo vs Apache CML: Matrix Operations



Indigo vs Eigen: SLAM kernels



Conclusions

- Domain-specific optimizations have significant impact on the performance of domain-specific applications
- Modular JIT compilers like Graal ease such optimizations through plugins
- Indigo demonstrates that SLAM applications written in Java can be significantly optimized using this approach

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Thank You!

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